Design Recipes for FPGAs: Using Verilog and VHDL

By Peter Wilson

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This book provides a rich toolbox of design techniques and templates to solve practical, every-day problems using FPGAs. Using a modular structure, it provides design techniques and templates at all levels, together with functional code, which you can easily match and apply to your application. Written in an informal and easy to grasp style, this invaluable resource goes beyond the principles of FPGAs and hardware description languages to demonstrate how specific designs can be synthesized, simulated and downloaded onto an FPGA. In addition, the book provides advanced techniques to create 'real world' designs that fit the device required and which are fast and reliable to implement.

- Examples are rewritten and tested in Verilog and VHDL
- Describes high-level applications as examples and provides the building blocks to implement them, enabling the student to start practical work straight away
- Singles out the most important parts of the language that are needed for design, giving the student the information needed to get up and running quickly

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